



Atty. Docket No. 42390P10070

PATENT

AMENDMENT UNDER 37 C.F.R. §1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2188

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Terry L. Kendall

Application No. 09/820,243

Filed: March 28, 2001

For: FLASH MEMORY LOW-LATENCY
CACHE

Examiner: Portka, Gary J

Art Unit: 2188

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JAN 27 2004

Technology Center 2100

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

RESPONSE AFTER FINAL ACTION UNDER 37 C.F.R. §1.116

Sir:

In response to the Office Action dated November 17, 2003, please consider the following
remarks:

Remarks/Arguments begin on page 2 of this paper.

I hereby certify that this correspondence is being deposited
with the United States Postal Service as first class mail with
sufficient postage in an envelope addressed to the Commissioner
for Patents, PO Box 1450, Alexandria, Virginia 22313-1450

on January 20, 2004

Deborah A. McGovern

W. McGovern

Signature Date